## IN THE UNITED STATES PA

Applicant:

Frank O'Mahony et al.

Title:

LOW LOSS INTERCONNECT STRUCTURE FOR USE IN MICROELECTRONIC

**CIRCUITS** 

Docket No.:

884.405US1

Filed:

June 27, 2001

Examiner:

Ahmed Sefer

Serial No.: 09/893023 Due Date: April 22, 2004 Group Art Unit: 2826

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard.

An Amendment and Response Under 37 C.F.R. 1.111 (10 Pages).

Authorization to charge Deposit Account No. 19-0743 in the amount of \$122.00 to cover the fee for additional claims as calculated below.

If not provided for in a separate paper filed herewith, If an additional fee is required due to changes to the claims, the fee has been calculated as follows:

CLAIMS AS AMENDED						
	(1) Claims Remaining After Amendment		(2) Highest Number Previously Paid For	(3) Present Extra	Rate	Fee
TOTAL CLAIMS	28	-	26	2	x 18.00 =	\$36.00
INDEPENDENT CLAIMS	4	-	3	1	x 86.00 =	\$86.00
[]MULTIPLE DEPENDENT CLAIMS PRESENTED						\$0.00
TOTAL						\$122.00

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

Atty: David R. Cochr

Reg. No. 46,632

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 24 day of March, 2004.

M. Richards

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)

<u>S/N 09/893023</u> <u>PATENT</u>

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Frank O'Mahony et al. Ex

Examiner: Ahmed Sefer

Serial No.:

09/893023

Group Art Unit: 2826

Filed:

June 27, 2001

Docket No.: 884.405US1

Title: 📐

LOW LOSS INTERCONNECT STRUCTURE FOR USE IN

MICROELECTRONIC CIRCUITS

Assignee:

Intel Corporation

Customer No: 21186

## AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

This responds to the Office Action mailed on January 22, 2004. Please amend the above-identified patent application as follows.

03/30/2004 WABRHAM1 00000106 190743 09893023

01 FC:1202 02 FC:1201 36.00 DA 86.00 DA